CS21 Decidability and Tractability

Lecture 7

January 20, 2023

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Outline

- equivalence of NPDAs and CFGs
- non-Context-Free languages via the CFL Pumping Lemma

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CFG example

- Arithmetic expressions over {+,*,(,),a}
  - (a + a) * a
  - a * a + a + a + a + a
- A CFG generating this language:

<expr> → <expr> * <expr>
<expr> → <expr> + <expr>
<expr> → (<expr>) | a

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CFG example

<expr> → <expr> * <expr>
<expr> → <expr> + <expr>
<expr> → (<expr>) | a

- A derivation of the string: a+a*a

<expr> ⇒ <expr> * <expr>
⇒ <expr> + <expr> * <expr>
⇒ a + <expr> * <expr>
⇒ a + a * <expr>
⇒ a + a * a

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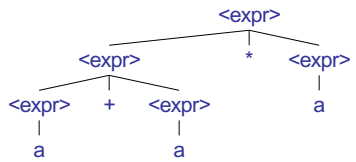
Parse Trees

- Easier way to picture derivation: parse tree



- grammar encodes grouping information; this is captured in the parse tree.

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CFGs and parse trees

<expr> → <expr> * <expr>
<expr> → <expr> + <expr>
<expr> → (<expr>) | a

- Is this a good grammar for arithmetic expressions?
  - can group wrong way (+ precedence over *)
  - different grammar for same language can force correct precedence/grouping

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Some facts about CFLs

- CFLs are closed under
  - union (proof?)
  - concatenation (proof?)
  - star (proof?)
- Every regular language is a CFL
  - proof?

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NPDA, CFG equivalence

Theorem: a language L is recognized by a NPDA iff L is described by a CFG.

Must prove *two* directions:

(⇒) L is recognized by a NPDA implies L is described by a CFG.

(⇐) L is described by a CFG implies L is recognized by a NPDA.

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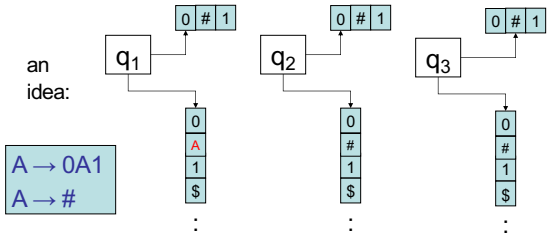
NPDA, CFG equivalence

Proof of (⇐): L is described by a CFG implies L is recognized by a NPDA.

an idea:



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NPDA, CFG equivalence

1. we'd like to non-deterministically guess the derivation, forming it on the stack
2. then scan the input, popping matching symbol off the stack at each step
3. accept if we get to the bottom of the stack at the end of the input.

what is wrong with this approach?

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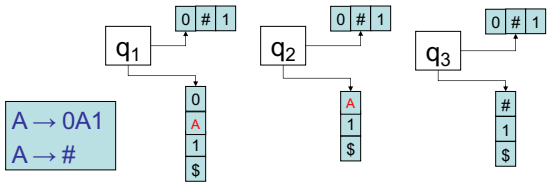
NPDA, CFG equivalence

– only have access to top of stack
– combine steps 1 and 2:
  - allow to match stack terminals with tape *during* the process of producing the derivation on the stack



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NPDA, CFG equivalence

- informal description of construction:
  - place $ and start symbol S on the stack
  - repeat:
    - if the top of the stack is a non-terminal A, pick a production with A on the lhs and substitute the rhs for A on the stack
    - if the top of the stack is a terminal b, read b from the tape, and pop b from the stack.
    - if the top of the stack is $, enter the accept state.

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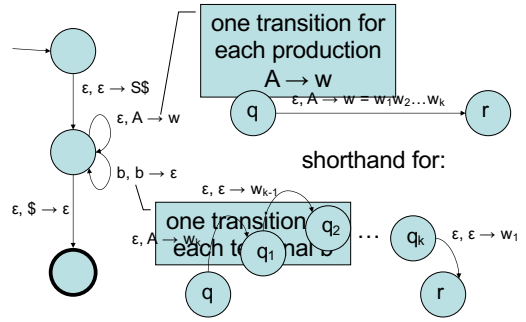
NPDA, CFG equivalence



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NPDA, CFG equivalence

Proof of (⇒): L is recognized by a NPDA implies L is described by a CFG.

- harder direction
- first step: convert NPDA into "normal form":
  - single accept state
  - empties stack before accepting
  - each transition *either* pushes *or* pops a symbol

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NPDA, CFG equivalence

- **main idea**: non-terminal $A_{p,q}$ generates exactly the strings that take the NPDA from state p (w/ empty stack) to state q (w/ empty stack)
- then $A_{start,\ accept}$ generates all of the strings in the language recognized by the NPDA.

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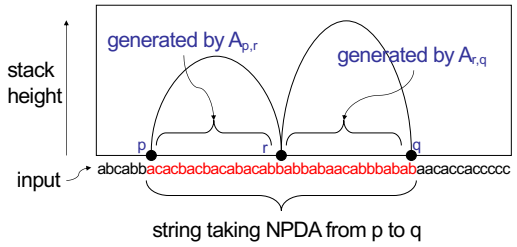
NPDA, CFG equivalence

- Two possibilities to get from state p to q:



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NPDA, CFG equivalence

- NPDA P = (Q, Σ, Γ, δ, start, {accept})
- CFG G:
  - non-terminals V = $\{A_{p,q} : p, q \in Q\}$
  - start variable $A_{start,\ accept}$
  - productions:

    for every p, r, q ∈ Q, add the rule

    $A_{p,q} \rightarrow A_{p,r}A_{r,q}$

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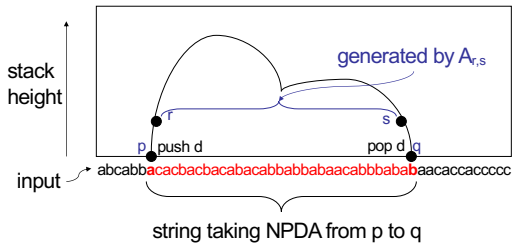
NPDA, CFG equivalence

- Two possibilities to get from state p to q:



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NPDA, CFG equivalence

- NPDA P = (Q, Σ, Γ, δ, s
- CFG G:
  - non-terminals V = {A
  - start variable $A_{start, ac}$
  - productions:

from state p, read a, push d, move to state r

from state s, read b, pop d, move to state q

for every p, r, s, q ∈ Q, d ∈ Γ and a, b ∈ (Σ ∪ {ε})
if (r, d) ∈ δ(p, a, ε), and
(q, ε) ∈ δ(s, b, d), add the rule
$A_{p,q} \rightarrow aA_{r,s}b$

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NPDA, CFG equivalence

- NPDA P = (Q, Σ, Γ, δ, start, {accept})
- CFG G:
  - non-terminals V = $\{A_{p,q} : p, q \in Q\}$
  - start variable $A_{start, accept}$
  - productions:

for every p ∈ Q, add the rule
$A_{p,p} \rightarrow \varepsilon$

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NPDA, CFG equivalence

- two claims to verify correctness:

1. if $A_{p,q}$ generates string x, then x can take NPDA P from state p (w/ empty stack) to q (w/ empty stack)
2. if x can take NPDA P from state p (w/ empty stack) to q (w/ empty stack), then $A_{p,q}$ generates string x

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NPDA, CFG equivalence

1. if $A_{p,q}$ generates string x, then x can take NPDA P from state p (w/ empty stack) to q (w/ empty stack)
   - induction on length of derivation of x.
   - base case: 1 step derivation. must have only terminals on rhs. In G, must be production of form $A_{p,p} \rightarrow \varepsilon$.

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NPDA, CFG equivalence

1. if $A_{p,q}$ generates string x, then x can take NPDA P from state p (w/ empty stack) to q (w/ empty stack)
   - assume true for derivations of length at most k, prove for length k+1.
   - verify case: $A_{p,q} \rightarrow A_{p,r}A_{r,q} \rightarrow^k x = yz$
   - verify case: $A_{p,q} \rightarrow aA_{r,s}b \rightarrow^k x = ayb$

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NPDA, CFG equivalence

2. if x can take NPDA P from state p (w/ empty stack) to q (w/ empty stack), then $A_{p,q}$ generates string x
   - induction on # of steps in P's computation
   - base case: 0 steps. starts and ends at same state p. only has time to read empty string ε.
   - G contains $A_{p,p} \rightarrow \varepsilon$.

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NPDA, CFG equivalence

2. if x can take NPDA P from state p (w/ empty stack) to q (w/ empty stack), then $A_{p,q}$ generates string x
   - induction step. assume true for computations of length at most k, prove for length k+1.
   - if stack becomes empty sometime in the middle of the computation (at state r)
     - y is read going from state p to r ($A_{p,r} \rightarrow^* y$)
     - z is read going from state r to q ($A_{r,q} \rightarrow^* z$)
     - conclude: $A_{p,q} \rightarrow A_{p,r}A_{r,q} \rightarrow^* yz = x$

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NPDA, CFG equivalence

2. if x can take NPDA P from state p (w/ empty stack) to q (w/ empty stack), then $A_{p,q}$ generates string x
   - if stack becomes empty only at beginning and end of computation.
     - first step: state p to r, read a, push d
     - go from state r to s, read string y ($A_{r,s} \rightarrow^* y$)
     - last step: state s to q, read b, pop d
     - conclude: $A_{p,q} \rightarrow aA_{r,s}b \rightarrow^* ayb = x$

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NPDA, CFG equivalence

2. if x can take NPDA P from state p (w/ empty stack) to q (w/ empty stack), then $A_{p,q}$ generates string x
   - if stack becomes empty only at beginning and end of computation.
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